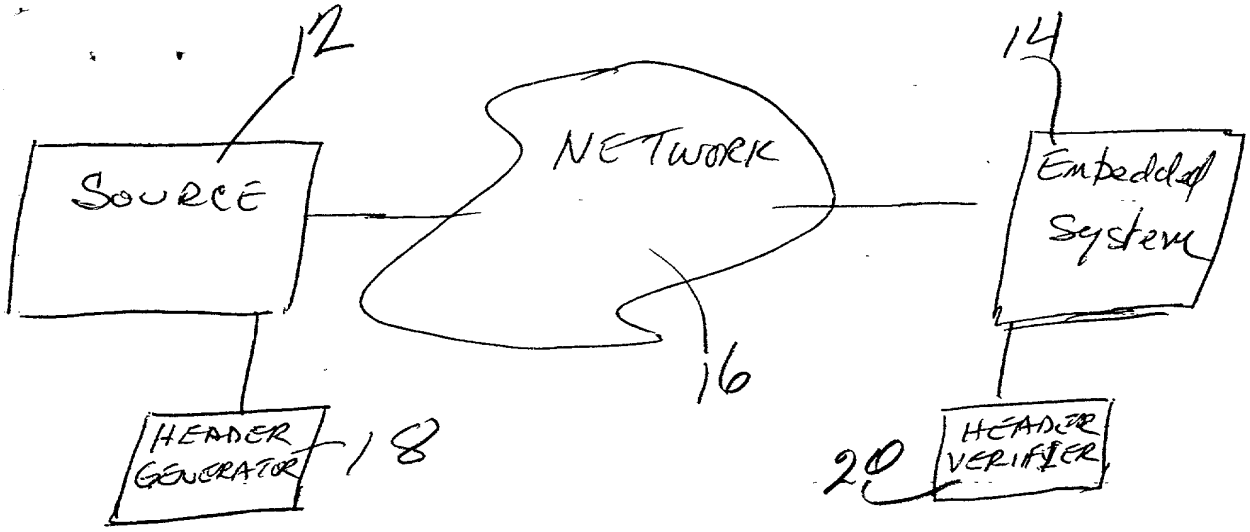
12
SOURCE
NETWORK
14
Embedded System
16
HEADER GENERATOR
18
20
HEADER VERIFIER

FIG. 1

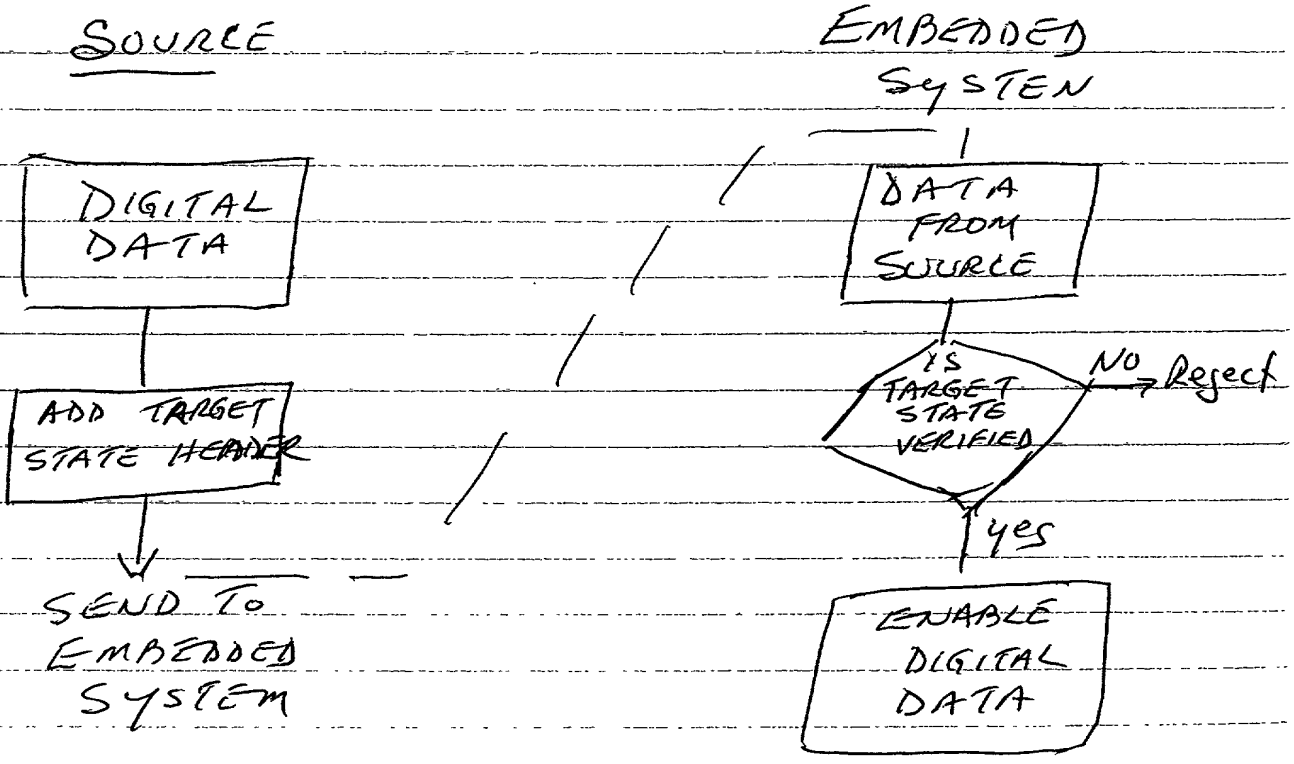
SOURCE
EMBEDDED SYSTEM
DIGITAL DATA
ADD TARGET STATE HEADER
SEND TO EMBEDDED SYSTEM
DATA FROM SOURCE
IS TARGET STATE VERIFIED
NO
Reject
yes
ENABLE DIGITAL DATA

FIG. 2

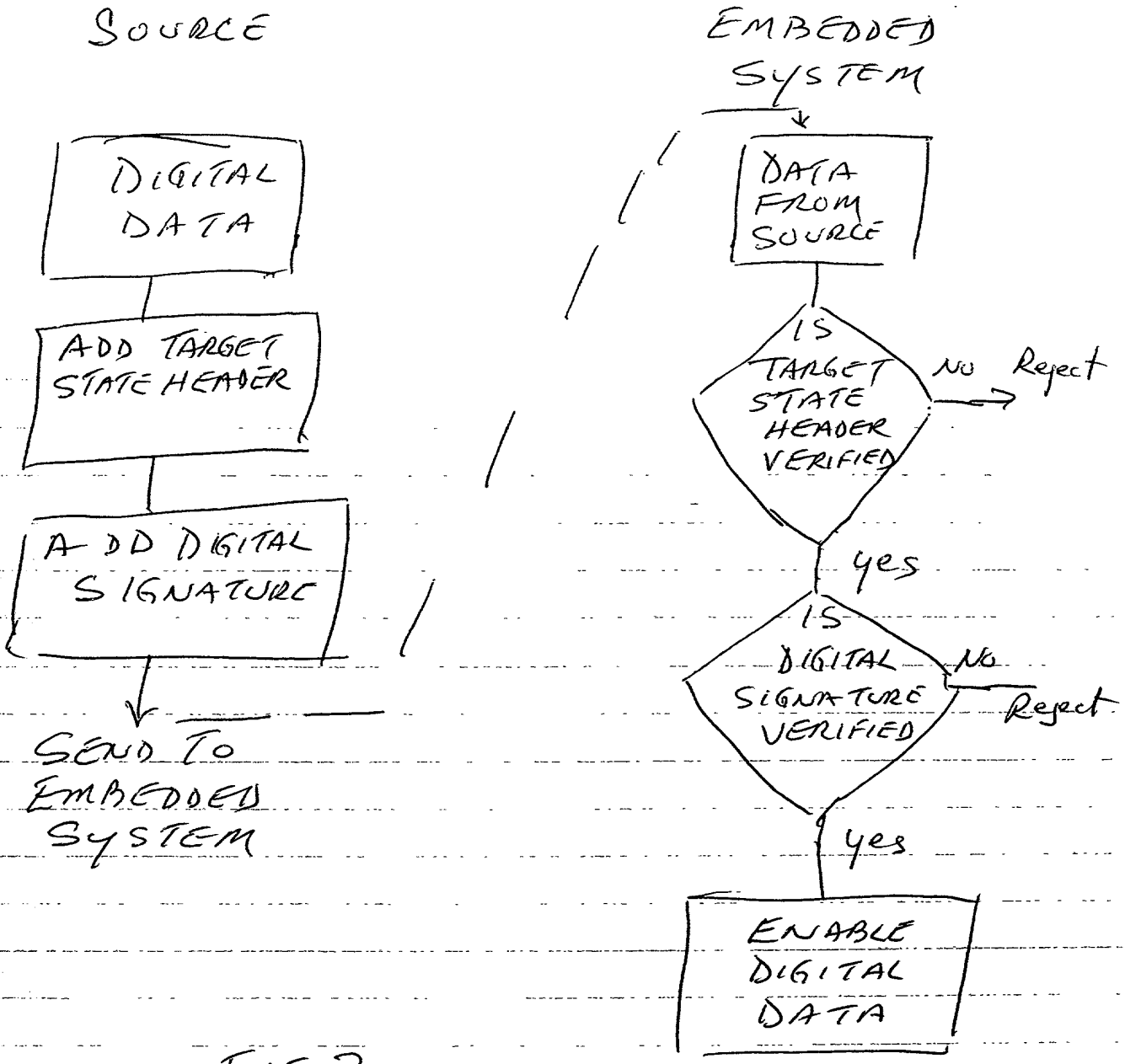
SOURCE
EMBEDDED SYSTEM
DIGITAL DATA
ADD TARGET STATE HEADER
ADD DIGITAL SIGNATURE
SEND TO EMBEDDED SYSTEM
DATA FROM SOURCE
IS TARGET STATE HEADER VERIFIED
NO
Reject
yes
IS DIGITAL SIGNATURE VERIFIED
NO
Reject
yes
ENABLE DIGITAL DATA

FIG 3

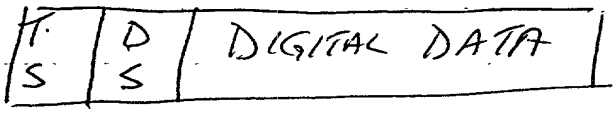
TS
DS
DIGITAL DATA

FIG 4